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Cross layer control for bounded shared state inconsistency in wireless IoT devices

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Abstract—The devices that constitute the Internet of Things are evolving to include more than just enabling sensing and actuation over a wireless interface. In a contemporary scenario, these devices perform tasks and contribute to an aggregate information flow, in a distributed manner. In the wake of this evolution, new distributed Internet of Things frameworks have emerged. These frameworks maintain a distributed shared state in a distributed hash table. An Internet of Things system's ability to make decisions autonomously and distributively depend on the level of consistency of the shared state. As wireless resources are scarce, the amount of deferred state information in each device is unknown when the system is highly utilised. In this paper, we have developed a controller with the objective to achieve a bounded time-average of deferred state information in each device while maintaining system stability. The controller is derived using Lyapunov drift optimisation with penalty. The resulting controller can successfully bound the shared state consistency level within a narrow margin, maintain system stability, and balance the traffic flow trade-off more successfully than comparable and conventionally used methods.

I. INTRODUCTION

As the Internet of Things (IoT) matures and enters new fields of operation, the notion of independent wireless sensors and actuators is becoming obsolete. Contemporary IoT applications are more focused on data generation and distributed decision making than simply connecting mundane household things for the sake of programmability and remote actuation. Forthcoming IoT applications and services will instead operate across multiple devices to, for example, accomplish a one-time task, operate a continuous process, or collect data. These applications will be composed and provisioned dynamically at runtime in a distributed manner to meet real-time demands. Additionally, IoT devices and their capabilities are discovered and managed in a manner reminiscent of micro services from the cloud computing domain, see Figure 1.

Emerging platforms such as Ericsson’s Calvin [1], will presumably enable and orchestrate these types of IoT applications and systems of IoT devices. To do so, these platforms rely on a shared distributed state realised using a Distributed Hash Table (DHT) [2]. The DHT contains the capabilities and state of the system’s devices and hosted applications. The information shared through the DHT allow devices to autonomously broker resources. This approach removes the need for centralised control and introduces some degree of platform fault tolerance. This also allows services and server-less functions to seamlessly migrate between IoT devices and cloud resources. Nevertheless, all autonomous distributed management decisions in such a system rely on a consistent DHT. Although a DHT is inherently fault tolerant, fine grained and mission critical decisions require near complete knowledge of the state’s consistency. The expected consistency of the DHT is thus linked to the operational efficiency of the system and its applications.

An IoT device, being it a thermometer or a multi-purpose robot, quintessentially connects to the world beyond, wirelessly. Devices that have sparse and intermittent power supplies or are entirely self-reliant must be frugal with how they use the wireless medium. Emerging 5th Generation wireless systems (5G) concepts are embracing these challenges faced by IoT devices [3]. Proposed 5G standards consider device limitations and intermittency, multiple medium access methods [4], Device-to-Device (D2D) communication, and new radio-access technologies [5]. The propositions of 5G are more about IoT and enabling mission critical applications than higher throughput.

In IoT devices, as applications and internal processes serve requests they generate both egress application data and updates bound for the DHTs. From now on we refer to the former as application traffic and the latter as state traffic. Although significant to the consistency of the system as a whole, the state traffic is not as latency sensitive as the application traffic. Application traffic and state traffic contend for the device’s wireless resources. The amount of state traffic versus application traffic produced by a service request is stochastic, as is the rate of service requests. Consequently, the state traffic will intrude on the application traffic. The trade-off between application traffic and state traffic is non-trivial. For example, naively prioritising the application traffic or capping the state traffic can disrupt the state traffic entirely. Doing so will ensure neither a favourable trade-off between the two traffic flows nor that the system will be stable. As the traffic and the wireless link connecting the device is stochastic and vary

Fig. 1. Forthcoming IoT application paradigm.
greatly over time, no absolute bounds on either traffic flow can be trivially set that accommodates both traffic flows. It is evident that maintaining a consistent DHT comes at a significant performance cost for the applications that the system is intended for. We therefore need to accept some level of DHT inconsistency. However, uncertainty in the DHT’s inconsistency has a detrimental effect on the quality of the system’s distributed decision-making.

In this paper, we propose a Cross Layer Controller that bounds DHT inconsistency uncertainty by bounding the time-average amount of deferred state traffic using a by using a virtual queue technique [6]. The proposed method is expressed as a jointly-formulated flow control and scheduling decisions derived using stochastic Lyapunov drift optimisation techniques found in [6], [7]. The proposed controller is thus able to accommodate the stochastic nature of the two traffic flows and ensure a bounded amount of deferred state information while maintaining queue and system stability. More precisely, the proposed controller schedules the two traffic flows so that the expected deferred state traffic is bounded at a predefined level without explicitly violating the application traffic. Intuitively, the deferred state traffic represents the amount of state information that would be lost if the device fails, or the level inconsistency of the of distributed state. This level can be dynamically adjusted to reflect each devices’ individual probability of failure, or a system-wide tolerated level of distributed state inconsistency. The resulting flow control decision is implemented and operated independently in each device while the scheduling decision is implemented centrally in the wireless infrastructure.

The authors of [8] stabilise a network with finite queues. This is not adequate for the problem presented in this paper as it needs to accommodate a variable target consistency while adequately accommodating the application traffic. In doing so one must be able to temporarily violate the target consistency to maintain a high throughput. Lyapunov drift analysis has been used extensively when analysing the performance and stability of mesh networks, such as [9]. Although related in terms of the characteristics of the devices, these works do not share the same objectives nor do they take into account the consistency of a shared data source, such as a DHT. The work [10] taxamonise methods for achieving consistent DHT-base routing algorithms in mesh networks. Although such methods are related they cannot be applied at the scale and rate of change of IoT systems. Additionally, in our previous work [11] we explored a similar trade-off but without explicit consistency objectives using Model Predictive Control (MPC).

Through simulation, we show that the proposed controller is able to bound the time-average shared state inconsistency within a narrow margin of the desired level while keeping the system stable. It is also shown that the controller is also able to do so while accommodating changes in load and wireless channel conditions. Additionally, the proposed controller is also able to more successfully balance the system’s two traffic flows than comparable and conventionally used methods.

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In this section, we define our system model for a device and the system’s infrastructure. As depicted in Figure 1, the target system consists of N devices and a Radio Base Station (RBS). To accommodate the traffic flows’ individual objectives, each device operates two egress traffic queues, one for each traffic class; application traffic $Q_i^a$ and state traffic $Q_i^s$. The state queue has a variable time-average limit $l_i(k)$. The limit $l_i(k)$, constitutes the bound on the deferred amount of state data. Both egress queues share the same sink, the wireless interface. All devices communicate over the wireless interface provided by the RBS. Each device has a time varying channel capacity of $\mu_i(k)$ in the time interval $[k,k+1]$. $\mu_i(k)$ has a particular probability distribution. Access to the medium is scheduled by the RBS at each time instance. For each device $i$, the capacity $\mu_i(k)$ can be shared between the application and state queues. Let $\mu_i^a(k)$ and $\mu_i^s(k)$ be the capacity allocated to the application and state queues for device $i$ at time period $k$, respectively and $\mu_i(k) = \mu_i^a(k) + \mu_i^s(k)$. The targeted system as a whole is depicted in Figure 2. An overview of the device model is illustrated in Figure 3.

Each device is subject to ingress application requests at the rate of $\lambda_i(k)$. The application requests are deposited in a FIFO reservoir and processed at a controllable rate $R_i(k)$. As a request leaves the reservoir, the device commits to processing the request, which subsequently results in a change to the DHT. Specifically, $R_i(k)$ is the number of requests committed at time $k$, and $\theta_i$ is the mean of the size of each request. $\beta_i$ is the fraction of $R_i(k)$ that is application traffic while $(1-\beta_i)$ of $R_i(k)$ is state traffic $\beta_i, \theta_i, \lambda_i(k)$ are constant for a device, but can be different among the devices, and are unique for each application $i$. 

![Fig. 2. Infrastructure model.](image1)

![Fig. 3. Device model with virtual queue $Q_i(k)$.](image2)

**II. SYSTEM MODEL**

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![Fig. 2. Infrastructure model.](image1)

![Fig. 3. Device model with virtual queue $Q_i(k)$.](image2)
III. CROSS LAYER CONTROLLER

In this section, we detail the premise of the target problem and formalise the proposed Cross Layer Controller. From here on, we refer to the controller as simply the controller. The controller is formulated using the Lyapunov drift-plus-penalty Theorem presented in [6]. In the resulting formulation, the controller has two control decisions: flow control and user (IoT device) scheduling. The two decisions are jointly formulated but act independently.

Contrary to the predictive approach in [11], this approach acts only on the system’s current state in each time instance. This allows the controller to accommodate the stochastic nature of the system’s queuing dynamics. In addition, the proposed controller can handle stochastic wireless channel conditions. The time-average horizon objective is achieved by acting on the relative changes in the system’s queue sizes, i.e., the Lyapunov drift.

A. Objective

The objective of the controller is to maximise the utility of the application queue while ensuring its stability, i.e., $E[Q^i_t(k)] < \infty$ and bound the time-average occupancy of the state queue i.e., $E[Q^i_t(k)] \leq l_i$. In a practical sense, the utility achieved by an IoT device is defined as the amount of requests $R_i(k)$ that is to be handled by that IoT device. The achievable utility depends on the designed flow controller, scheduling decisions, and the network capacity. We define a strictly increasing utility function for the application queue $i$, $\forall i$ as a function of output rate of the application request reservoir $R_i(k)$, as follows: let $r_i$ be the time-average utility of the application queue $i$,

$$r_i = \lim_{k \to \infty} \frac{1}{k} \sum_{\tau=0}^{k-1} E[\beta_i R_i(\tau)]$$  \hspace{1cm} (1)

The objective of the controller is formally expressed as,

$$\max \sum_{i=1}^{N} g(r_i)$$  \hspace{1cm} (2)

subject to

$$E[Q^i_t(k)] \leq l_i \hspace{1cm} \forall i$$  \hspace{1cm} (3)

$$E[Q^i_t(k)] < \infty \hspace{1cm} \forall i$$  \hspace{1cm} (4)

We note that imposing the constraints in Equation (3) make the solution of our optimisation problem complicated and the problem is actually a complex Markov Decision Problem (MDP). It is well-known that MDP suffers from the curse of dimensionality since the number of queue state vectors grows geometrically with the number of IoT devices. Therefore, it is not feasible to find a suitable algorithm in practice. In this paper, instead of finding an optimal solution to the problem Equations (2) to (4), we develop a much simpler sub-optimal controller which is based on Lyapunov drift technique [6].

B. Queuing dynamics

Let us first define the system’s queue dynamics. The application and state queue’s dynamics of IoT device $i$ are as follows,

$$Q^i_t(k+1) = \max \{Q^i_t(k) - \mu^i_t(k), 0\} + R_i(k) \theta_i \beta_i$$

To satisfy the constraint in [3] we define a virtual queue (see Figure 2) with the following dynamics,

$$Y_i(k+1) = \max \{Y_i(k) - I_i(k), 0\} + Q^i_t(k+1)$$ \hspace{1cm} (5)

$$= \max \{Y_i(k) - I_i(k), 0\} + \max \{Q^i_t(k) - \mu^i_t(k), 0\} + R_i(k) \theta_i (1 - \beta_i)$$

The virtual queue $Y_i(k)$ for application $i$ in Equation (5) accumulates that application’s amount of deferred state data in $Q^i_t(k)$ and deducts its bound on the deferred amount of state data $I_i(k)$. Consequently, as long as the virtual queue $Y_i(k)$ is stabilised then the constraint in Equation (3) will be satisfied. This can be achieved by using Lyapunov Optimisation Drift, as we show next.

C. Lyapunov drift

In this paper we employ a quadratic Lyapunov function expressed as,

$$L(k) = \frac{1}{2} \sum_{i=1}^{N} Q^i_t(k)^2 + Y_i(k)^2$$

This Lyapunov function is the scalar measure of the total of both actual and virtual queues in the network. The Lyapunov drift is given by,

$$\Delta L(k) = E[L(k+1) - L(k) | \mathcal{F}_k]$$  \hspace{1cm} (6)

where $\mathcal{F}_k = \{\{Q^i_t(k), Y_i(k)\}, \ldots, \{Q^N_t(k), Y_N(k)\}\}$. It is a well-established result [6] that minimising the following function,

$$\min \Delta L(k) - \sum_{i=1}^{N} V E[g(R_i(k))]$$ \hspace{1cm} (7)

minimises Equation (2). Clearly, it enables us to have a simple multi-objective optimisation problem where the first term (the Lyapunov drift) satisfies the constraints in Equations (3) and (4), whereas the second term $\sum_{i=1}^{N} V E[g(R_i(k))]$ maximises the total network utility as expressed in Equation (2). The drift-plus-penalty theorem in [6] does not require strict convexity assumptions. However, it ensures that the expected of the optimisation problem’s primal converges to a solution that is within a factor of optimality, with bounded queue sizes. Furthermore, $V$ is a system parameter which allows us to make a trade-off between the achieved utility and the average queue backlog in the queues, as applied in [6], [7]. Next, we design a controller which minimises Equation (7) at each period $k$.

D. Controller design

Since we do not have an explicit close-form expression for Equation (7), in order to formulate a controller that minimises Equation (7) in discrete time, we will first have to find the bound for Equation (6). We proceed with the following in mind: $\max \{x, 0\}^2 \leq x^2$ and $\max \{x, 0\} \leq x^2$ to express,
We note that the network capacity at a time period $k$ is always upper-bounded due to practical limitations (e.g., transmit power). That is to say, $\mu^i_t(k) \leq \mu_{\text{max}}$, $\mu^i_t(k) \leq \mu_{\text{max}}$ for all $i$ and $k$. Also, the amount of requests that can be admitted is upper-bounded, i.e., $R_i(k) \leq R_{\text{max}}$, for all $i$ and $k$. $\mu_{\text{max}}$ and $R_{\text{max}}$ are constants. The choice of $R^i_{\text{max}}$ is independent of the stability of the system. However, a choice of $R^i_{\text{max}}$ less than the allocated aggregate mean channel capacity results in a strictly underutilised system. Then we have,

$$Q^i_t(k+1)^2 - Q^i_t(k)^2 \leq B^i - 2Q^i_t(k)(\mu^i_t(k) - R^i_{\text{max}}(k)\beta_i\theta_i),$$

where $B^i = \mu^2_{\text{max}} + (R_{\text{max}}\beta_i\theta_i)^2$. Similarly, we find a bound for the virtual state queue $Y_i(k)$ as follows,

$$Y_i(k+1)^2 - Y_i(k)^2 \leq l_i(k) - 2Y_i(k)l_i + Q^i_t(k)^2 + 2Y_i(k)(Q^i_t(k) + R_i(k)(1 - \beta_i)\theta_i) + (R_i(k)(1 - \beta_i)\theta_i)^2 + \mu^i_t(k)^2 - 2Q^i_t(k)(\mu^i_t(k) - R_i(k)(1 - \beta_i)\theta_i) \leq B^i - Q^i_t(k)^2 - 2Y_i(k)(l_i - Q^i_t(k) - R_i(k)(1 - \beta_i)\theta_i) - 2Q^i_t(k)(\mu^i_t(k) - R_i(k)(1 - \beta_i)\theta_i)$$

Minimising the right-hand side of the inequality in Equation (10) ensures queue stability and maximises our objective in Equation (9). Next, we provide our controller, which jointly minimises the right-hand side of Equation (10) at each $k$.

1) Flow control: The flow control decision’s objectives is to regulate the flow $R_i(k)$ from the application request reservoir. We proceed by collecting the expressions from Equation (10) that include $R_i(k)$ which is given as,

$$\sum_{i=1}^{N} E[2Y_i(k)R_i(k)(1 - \beta_i)\theta_i + 2Q^i_t(k)R_i(k)(1 - \beta_i)\theta_i + 2Q^i_t(k)R_i(k)\beta_i\theta_i - V_g(R_i(k))]$$

Minimising the above expression for each device $i$ at each $k$ will minimise the right-hand side of Equation (10). Each device $i$ solves the following problem, which will determine the flow decision for that device at time $k$,

$$\arg \min_{R_i} R_i|Q^i_t(k)(1 - \beta_i)\theta_i + Q^i_t(k)\beta_i\theta_i + Y_i(k)(1 - \beta_i)\theta_i - V_g(R_i)$$

s.t. $R_i \leq R^i_{\text{max}}$

The choice of $R^i_{\text{max}}$ is independent of the stability of the system. $V$ is a parameter that determines the balance between the controller’s desire to back pressure requests and maximise its utility through $R_i$. Any positive $V$ will result in a stable system.

Furthermore, the flow control is at its most effective when it keeps up with the system’s rate of change, $r_{\text{low}}(k) = \min\{l_i(k), \mu^i_t(k)\mu^i_t(k)/(\mu^i_t(k) + \mu^i_t(k))\}$. However, a lower rate does not violate the stability criterion.

2) Scheduling: The other control decision in Equation (10) is the scheduling decision (i.e., determination of $\mu^i_t(k)$ and $\mu^i_t(k)$). The scheduling decision is centralised to the RBS. The idea is that $\mu^i_t(k)$ and $\mu^i_t(k)$ are determined in a way that the right-hand-side of Equation (10) is minimised. Grouping and maximising in terms of $\mu^i_t(k)$ and $\mu^i_t(k)$ from Equation (10) yields,

$$\arg \max_i \max\{\mu^i_t(k)\mu^i_t(k), Q^i_t(k)\mu^i_t(k)\} \quad (11)$$

This essentially implies that the largest queue amongst all the devices will receive the channel capacity at that time $k$.

E. Parameter estimation

The distribution of the parameters $\theta_i$ and $\beta_i$ are unknown to the system at runtime. The mean of the parameters are estimated using stochastic gradient descent, $\theta_i^{k+1} \leftarrow \theta_i^k - \gamma \nabla F(\theta_i^k)$, where $\gamma = \gamma \forall i$.

IV. EVALUATION

In this section, we motivate and taxonomise the evaluation scenarios for the proposed Cross Layer Controller. To evaluate the controller, a simulator was developed in Python using SimPy [12] for queue representations and the simulation core. CVXPY [13] is used for solving convex optimisation problems. For the sake of reproducibility, the simulator and the simulation below have been made available at [14]. The simulations presented below are designed to reveal how the proposed controller can maintain the maximum time-average deferred state information, converge, and handle variations in inputs.

A. Comparison policies

To evaluate the proposed controller we introduce three contrasting scheduling policies.

Round Robin (RR) scheduling In RR scheduling, the queues are scheduled in a sequential order regardless of
their occupancy, i.e., this approach does not enforce an explicit scheduling objective. RR is a commonly used scheduling approach.

**Prioritised scheduling** In prioritised scheduling, the queues in the system are partitioned into different prioritisation sets. The set of queues in a prioritisation level are never scheduled unless the queues in the higher prioritisation levels are empty. In this scenario the \( Q_i^P(k) \) is prioritised over \( Q_i^L(k) \) to ensure minimum application latency.

**Priority Threshold (PT) scheduling** In PT scheduling the queues in the system are partitioned into two sets. One of the sets has an occupancy threshold. A queue in that set is only scheduled if its occupancy exceeds the threshold. In the simulations below, the set of \( Q_i^P(k) \) are assigned a threshold of \( l_i(k) \).

**B. Metrics**

The metrics below are used to evaluate the proposed controller.

**Utilisation** The utility level of a system of queues is expressed as \( \rho = \bar{\lambda}/\mu \). In the simulations below we observe two utilisation factors; end-to-end utilisation for device \( i \) \( \rho_{i,e2e}^P(k) \) and the resulting level of utilisation due to back pressure from the flow control decision \( \rho_{i,flow}^P(k) \). We also define \( \bar{\rho}_{i,e2e} \) as the time designed time-average utilisation of a device as determined by the load and system parameters in Table I. Furthermore, \( \bar{\rho}_{i,e2e}^P(k) \) and \( \bar{\rho}_{i,flow}^P(k) \) are the instantaneous time-averages for system and post-controller utilisation levels, respectively, over all \( N \) devices. If the system is stable, \( E[\rho_{i,flow}] \leq 1 \). Consequently, \( \bar{\rho}_{i,e2e}^P(k) \geq 1 \) is a system load over 100%.

**Utility** The utility of the system is proportional to \( R \).

**Trade-off** The trade-off between the competing traffic flows is at the core of the problem addressed in this paper. We formally express this trade-off as,

\[
\alpha(k) = \begin{cases} 
\frac{Q_i^L(k)-l_i(k)}{l_i(k)} & \text{if } \rho_{i,e2e}^L(k) \geq 1 \\
|\Delta Q_i^L(k) - \Delta Q_i^P(k)| & \text{if } \rho_{i,e2e}^L(k) < 1
\end{cases}
\]

Where \( \Delta \) is the forward difference of \( f(x) \), \( \Delta f(x) = f(x+1) - f(x) \). The first component premiers stability and a bounded \( Q_i^L(k) \) when the system is overloaded. The second component premiers that \( Q_i^L(k) \) and \( Q_i^P(k) \) change at a similar rate when the system is underutilised. A low value is desired.

We also define \( \tilde{\lambda}_i(k), \tilde{\mu}_i(k), \tilde{\rho}_{i,e2e}^P(k), \) and \( \tilde{\rho}_{i,flow}^P(k) \) as the instantaneous mean for those values over all \( N \) devices.

**C. System parameter values**

The simulation parameters used to evaluate the proposed controller are presented in Table I. For this scenario we assume a Poisson arrival process and exponential service times. In the simulation scenario below, the total system utilisation \( \bar{\rho}^e(k) \) is intermittently over 100%, \( \bar{\rho}^e(k) > 1 \), see Figure 4. Furthermore, for the policies detailed in Section IV-A \( \bar{\rho}_i(k) = \lambda_i(k) \) \( \forall i \).

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sim. duration</td>
<td>300</td>
</tr>
<tr>
<td>( \lambda_i(k) )</td>
<td>Poisson process, ( \exp(\lambda = 0.24) ) ( \forall i ), see Figure 4</td>
</tr>
<tr>
<td>( \mu_i(k) )</td>
<td>See Figure 4</td>
</tr>
<tr>
<td>( \rho_{flow}^P(k) )</td>
<td>0.01</td>
</tr>
<tr>
<td>( R_i^{Max} )</td>
<td>0.05 ( \forall i )</td>
</tr>
<tr>
<td>( \theta_i )</td>
<td>( \mathcal{N}(\mu = 3, \sigma^2 = 3/2) ) ( \forall i ), sampled for each arrival</td>
</tr>
<tr>
<td>( \beta_i )</td>
<td>( \mathcal{U}(0.35, 0.65) ), sampled once</td>
</tr>
<tr>
<td>( l_i(k) )</td>
<td>See Figure 4</td>
</tr>
<tr>
<td>( \gamma )</td>
<td>0.01 ( \forall i )</td>
</tr>
<tr>
<td>( V )</td>
<td>16</td>
</tr>
<tr>
<td>( g_i(R) )</td>
<td>( g_i(R) = \ln \beta_i R_i(k) )</td>
</tr>
</tbody>
</table>

**TABLE I**

**SIMULATION PARAMETER VALUES OF THE PROPOSED CONTROLLER.**

![Fig. 4. System parameters and designed utilisation.](image)

**D. Input values**

When evaluating the proposed Cross Layer Controller, we are particularly interested in the instances when the system is overutilised. We therefore balance the system inputs to achieve a desired level of system utilisation as presented by \( \bar{\rho}^e(k) \) in Figure 4. This is achieved by either varying \( \lambda_i(k) \) or \( \mu_i(k) \). The system utilisation depicted in Figure 4 has three different load scenarios recurring over five time periods. The three load scenarios are; strictly underutilised, strictly overutilised, and transient. They are distributed over five periods, as demarked in Figure 4.

After the epoch of intermittent overutilisation \( t > 600 \), system load enters the transient epoch where the designed utilisation drops to 55%, \( \bar{\rho}^e(k) = 0.55 \). Up until this point the change in \( \bar{\rho}^e(k) \) was attributed change in \( \lambda_i(k) \). After this point \( t > 600 \), \( \lambda_i(k) \) is kept constant and \( \mu_i(k) \) is varied as a sinusoid, see Figure 4. From now on we refer to the intermittent overutilisation and transit epochs as epoch 1 and epoch 2, respectively.

**V. RESULTS**

In this section we present the results from the simulations detailed in Section IV. Note that the time scale in the simulations is unit-less. We also note that the estimations for \( \sigma_i \) and \( \beta_i \) have converged after 100 time units.

**A. Expected deferred state traffic**

The design objective of the controller is \( E[Q_i^e] \leq l_i \) for each device \( i \). Throughout epoch 1, as the system is overutilised, the objective of the controller is to keep \( E[Q_i^e] = l_i \). Figure 5 shows that the controller is able to maintain a time-average within the 95th percentile < 1% of \( l_i(k) \).
$l_i(k)$ is increased to 20 at $k = 200$ and then decreased back to 10 at $k = 400$. We note that $\hat{Q}(k)$ tracks the target $l_i(k)$ practically immediately. The quick adjustment is achieved by momentarily sacrificing instantaneous stability $\hat{\rho}^{\text{flow}}(k) > 1$. This is practically instantly compensated for by a proportional reduction in $R_i(k)$. The expected stability of the system is thus maintained.

At the beginning of epoch 2, $\hat{\rho}^{\text{flow}}(k)$ lags $\hat{\rho}^{\text{e2e}}(k)$ and $\hat{\rho}^{\text{e2e}}(k)$ as the controller accommodates accumulated deferred application requests. Between $k = 600$ and just before $k = 680$ the controller gradually balances the application traffic and state traffic to when $\hat{\rho}^{\text{flow}}(k) < 1$. After this point there is no contention between the two traffic flows and $\hat{Q}$ drops to just above 0, see Figure 5.

In epoch 2, $\lambda_i(k)$ and $l_i(k)$ are kept constant and $\mu_i(k)$ is oscillated sinusoidally around $\pm10\%$ of $\hat{\rho}^{\text{e2e}}(k) = 1$. The scheduling component of the controller will be first to act on the change in $\mu_i(k)$. Consequently, there is small lag in $\hat{R}(k)$ from the change in $\hat{\rho}^{\text{e2e}}(k)$, see Figure 5. However, this lag has evidently no practical impact on the controller’s ability to track $l_i(k)$ in relation to $\hat{\rho}^{\text{e2e}}(k)$.

Furthermore, PT scheduling has the objective $E[Q_i^a] = l_i$. As Figure 6 shows, PT scheduling is able to meet this objective while the system is overutilised. However, contrary to the proposed controller, as the system moves into phase 2, $\hat{Q}(k)$ does not diminish. This is because none of the state queues will be served as long as there are any non-empty application queues $\sum_{i=1}^{N} Q_i^a(k) > 0$ or $Q_i^s(k) \geq l_i \forall i$ in the system. This is an undesirable trade-off for the targeted system as we do wish to reduce the amount of deferred state information when possible.

Fig. 6. $\hat{Q}(k)$ and $l_i(k)$ for PT scheduling.

B. Stability and system utility

Another objective of the controller is to maintain a stable time-average system of queues, namely $E[Q_i^a] < +\infty$ and $E[Q_i^s] < +\infty$ for each device $i$. The system’s stability is revealed by observing the growth of the queues $\bar{Q}(k)$ and $Q_i^s(k)$ as well as the system utilisation after back pressure is applied, $\hat{\rho}^{\text{flow}}(k)$. As illustrated by Figure 7 both conditions: $E[Q_i^a] < +\infty$ and $E[Q_i^s] < +\infty$ are met using the proposed controller.

In Figure 7, note that $\hat{\rho}^{\text{flow}}(k)$ persist a level of around 1 even though $\hat{\rho}^{\text{e2e}}(k) \geq 1$. This is because the controlled system administers deferred application requests that have been back pressured. This is as designed and means that the system of $N$ devices, as well as all application queues and service queues are stable.

Furthermore, the back pressure effect can be directly observed through $\hat{R}(k)$ in Figure 5. Note the inverse relationship between $\hat{R}(k)$ and $\hat{\rho}^{\text{flow}}(k)$ in Figure 7.

Neither of the methods; RR, PT, nor Prio. have stability as an objective. This is made evident by their growing queue sizes while $\hat{\rho}^{\text{e2e}}(k) \geq 1$ in Figure 7. In the case of RR scheduling, as it does not discriminate between two traffic flows, they grow with a similar gradient. Prioritised scheduling on the other hand prioritizes $Q^a$ and thus lets $Q^s$ grow uncontrollably. PT scheduling inadvertently maintains a stable $Q^s$ at the expense of $Q^a$ stability.

Because the proposed controller practices back pressure, it will analogously achieve a lower utility than the other scheduling methods as $R_i$ is in this case suppressed. RR, PT, and Prio., will on the other hand persist $R_i(k) = \min\{R_{\text{max}}, \lambda_i(k)\}$. This difference is contrasted in Figures 5 and 6 for the controller and the other methods respectively. The difference is small, but operating with a utility beyond the stability point has a detrimental effect on the latency experienced by each serviced application request.

C. Choice of $V$

As discussed in [6], [7], although any positive $V$ will result in a stable system, the choice of $V$ does have an impact on the controller’s ability to meet its other objectives. A large $V$ will promote the utility function, while a small $V$ will promote back pressure. This is reflected in the immediate value of $R$ at each $k$. However, all values of $V$ will achieve the same time-average $R$, see Figure 8.
Furthermore, as illustrated by Figure 8, the choice of $V$ has an impact on the sample variance of $Q_i(k)$ and $\hat{\alpha}^2 Q_i(k)$. Although the time-average converges at $V > 10$, it is desirable to minimise $\hat{\alpha}^2 Q_i(k)$ provided it is not at the expense of system utility $R_i(k)$. We can also note that $R$ is not affected by $V$. As illustrated in Figure 8, $\hat{\alpha}^2 Q_i(k)$ is convex to $V$. For this particular system, minimum $\hat{\alpha}^2 Q_i(k)$ is at $V = 16$.

D. Quantifying the trade-off

We have now established that the controller successfully bounds the size of $Q_i(k)$ to $l_i(k)$ when the system is overutilised $\tilde{\rho}^{\text{exec}}(k) \geq 1$ and that the controller is able to stabilise the system. The trade-off metric formulated in Section IV-B attempts to summarise the performance of the controller in comparison to the other scheduling methods presented in Section IV-A over time. In terms of the metric Section IV-B, the controller performs strictly better than the comparison scheduling methods. Therefore, for increased contrast, Figure 9 shows the cumulative trade-off for each method subtracted by the cumulative trade-off for the controller. PT scheduling tracks the proposed controller well but fails to balance the two flows as $\hat{\rho}^{\text{flow}}(k) < 1$ in the underutilised epoch. Prioritised scheduling allocates, on average, no resources to $Q$ which therefore always maintains a high occupancy. RR scheduling indiscriminately schedules $Q^i$ and $Q^o$ and thus achieves a middle-of-the-road value during overutilisation.

Also worth noting is that

VI. CONCLUSIONS

In this paper, we have presented a Cross Layer Controller for achieving both a predictable maximum time average shared state inconsistency and system stability. The proposed controller and system objectives were formulated using Lyapunov Drift optimisation with penalty. The resulting Cross Layer Controller was verified through simulation. The simulator revealed that the proposed Cross Layer Controller can track the desired level of shared state inconsistency within a narrow margin. It was also shown that the Cross Layer Controller achieves system stability and can more successfully balance the system’s two traffic flows than comparable and conventionally used methods. Additionally, the proposed Cross Layer Controller can accommodate both momentary stochasticity in the queues and rapid changes in set points $l_i$ while maintaining the desired time average shared state inconsistency.

Possible extensions of this work include a down-link scheduling policy for scheduling entire application processes across the system’s IoT devices. To capture the energy constrained nature of IoT devices, such a system controller can include a maximum time-average transmission energy for each device.

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