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TRUE EQUIVALENT CHIP THICKNESS FOR TOOLS WITH A NOSE RADIUS

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Abstract: A majority of the established systems for choice and optimization of cutting data are based on Woxén’s equivalent chip thickness, \( h_{ew} \). In metal cutting theory and models, the equivalent chip thickness is of vital importance when the depth-of-cut \( a_p \) is in the same order or smaller than the nose radius \( r \). Woxén made considerable simplifications in his chip area model, that form the basis for calculations of the equivalent chip thickness. Basic mathematical solutions, e.g. describing the chip area on circular inserts, are lacking. This article describes the geometrical implications when machining with round inserts. The error in Woxén’s equivalent chip thickness is largest when the depth-of-cut is less than \( \frac{1}{4} \) of the nose radius. The calculations of the equivalent chip thickness based on the Woxén model are up to 50 % wrong, for some combinations of cutting data in the finishing range. The presented results explain the difficulties in getting a good validity in the models used to calculate tool life in finishing machining. The error leads to an underrating of the tool load in many machining situations.

Keywords: Woxén, equivalent chip thickness, metal cutting, round inserts.

1. INTRODUCTION

When machining with an insert with a nose radius the theoretical chip thickness will vary, from the major cutting edge, along the nose radius, to the minor cutting edge. The theoretical chip thickness \( h_1 \) is together with the cutting speed \( v_c \) the two most important factors that influences the functionality and productivity of the cutting process.

To produce a cut surface with acceptable properties, the cutting tool must have a curved bridging between the major and the minor cutting edge. In many cases a base geometry in the form of a circular arc with a standardized radius is used. Inserts with a pronounced nose radius are most commonly used in turning operations.

Using a constant approach angle and a constant feed, the major cutting edge will cut a chip with a constant theoretical chip thickness. The tool load will vary along the tool nose due to the variation in theoretical chip thickness. Woxén (Woxén 1932) introduced an equivalent chip thickness, \( h_{ew} \), in 1932, with the purpose to use it as a characteristic parameter describing the mean theoretical chip thickness along the tool nose.

The stresses in a cutting tool are approximately proportional to the theoretical chip thickness \( h_1 \). This means that \( h_1 \) has a dominant influence on the tool wear and the tool life.

Along the tool nose, \( h_1 \) will vary from its maximum value down to 0. When machining with an \( a_p \) less than the size of the nose radius, varying cutting conditions will rule along all of the active cutting edge. Models describing tool wear and tool life will be dramatically simplified if a characteristic or equivalent value of \( h_1 \) can be introduced.

Woxén's equivalent chip thickness describes a kind of theoretical mean chip thickness, based on the active cutting edge length. Another interpretation of the equivalent chip thickness is that it joins together combinations of significant cutting parameters into one single parameter. Woxén's representation gives a value of the equivalent chip thickness for different choices of feed \( f \), depth-of-cut \( a_p \), approach angle \( \kappa \) and nose radius \( r \).

An increased depth-of-cut means that the major cutting edge increases its part of the process energy conversion. This also means that \( h_{ew} \) will approach the current value of \( h_1 \) for the major cutting edge. For a smaller depth-of-cut, \( h_{ew} \) will significantly differ from this value.

Most systems for optimization and choice of cutting data use the equivalent chip thickness. Recommended cutting data from tool and material catalogues are often based on \( h_{ew} \).
2. LIST OF SYMBOLS

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<thead>
<tr>
<th>Symbol</th>
<th>Description</th>
<th>Unit</th>
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<td>A</td>
<td>True chip area</td>
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<td>A_W</td>
<td>Woxén’s chip area</td>
<td>mm²</td>
</tr>
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<td>h_eW</td>
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</tr>
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<td>\kappa</td>
<td>Approach angle</td>
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3. PROBLEM DESCRIPTION

Based on experience, it is very hard to predict tool life in finish machining operations. There are several reasons why rough machining easier can be described in tool life models, than finish machining.

One of the reasons is how the theoretical chip thickness behaves, as a constant when rough machining with large values of \( a_p \), compared to the significant variation for \( a_p < r \).

If studied closely, it can be concluded that Woxen’s equivalent chip thickness has quantitative imperfections for \( a_p < r \). Below, a modified version of Woxén’s equivalent chip thickness is derived and presented. This modified equivalent chip thickness \( h_e \) is based on Woxén’s fundamental conditions according to equation 1, where he is given by the ratio between the chip area A and the active cutting edge length l_c.

\[
h_e = \frac{A}{l_c} \quad (1)
\]

4. DELIMITATIONS

In the presented work, a simple circular bridging between the major and minor cutting edge is used. This geometry is the most commonly used in turning. The attempt is based on the condition that the bridging between the major and minor cutting edge is represented by a part of a circular arc. The depth-of-cut is limited to the range less than the nose radius r.

5. REALIZATION

The mathematical calculations were performed using the mathematical software Mathcad, versions 11 and 14.

6. THE EQUIVALENT CHIP THICKNESS \( h_e \)

Woxen approximates the chip area A using the product between depth-of-cut \( a_p \) and feed f. This approximation results in computational errors that are not insignificant in under certain conditions. The equivalent chip thickness according to Woxén can be calculated as:

\[
h_eW = \frac{A_W}{l_cW} = \frac{a_p \cdot f}{r \cdot \sin \kappa + \frac{f}{2}}
\quad (2)
\]

where \( A_W \) is Woxén’s chip area and \( l_cW \) is the length of the cutting edge that is active in the cutting process. The active cutting length is built by 3 parts, a linear part which is the major cutting edge to the tangation point to the tool nose, the nose part which is equivalent to \( \kappa \cdot r \), and one final part that is approximated by \( f/2 \), according to Figure 1.

The latter approximation is considered to be acceptable from an accuracy viewpoint. In Woxén’s model, the tool nose is straightened out, forming a rectangular area that describes the chip area according to Figure 2.

![Figure 1 Representation of the active cutting length into 3 parts, \( l_{cI} + l_{cII} + l_{cIII} \).](image)
7. TRUE EQUIVALENT CHIP THICKNESS

The design of an insert between the major and the minor cutting edge, is normally achieved through a nose radius $r$. Along the tool nose the theoretical chip thickness will start at its nominal value and successively decrease to a value of 0 over the minor cutting edge, according to Figure 3.

The relations between theoretical chip thickness $h_1(\delta)$, feed $f$, and nose radius $r$ can be identified in Figure 4 and calculated according to the equation system according to equation 3.

The angular position for $h_1=0$ in Figure 5 can be calculated out of equation 4 by inserting $h_1(\delta)=0$, after which the angle $\delta_0$ can be obtained through equitation 5.
An equation describing the angular function $\delta_{ap}$ can be formulated according to equation 6, by studying the large triangle in Figure 6.

$$r^2 = (f + (r - a_p) \tan \delta_{ap} )^2 + (r - a_p)^2$$

(6)

$$\delta_{ap} = \tan^{-1} \left( \frac{ - f + \frac{2 \cdot r \cdot a_p - a_p^2}{r - a_p} - a_p }{r \cdot a_p} \right)$$

(7)

Figure 6. The appearance of the chip area for machining cases where $a_p < r$.

Figure 6 and Figure 7 illustrate the chip area when $a_p < r$. The chip area can be identified as the sum of 3 different surface elements, the surface $A$ according to equitation 7 with integration limits $(\delta_0, \delta_{ap})$, the triangular surface $A_t$, and the segmental surface $A_c$. The segmental surface $A_c$ can be calculated by using the chordal formula, $A = 0.5 \cdot r^2 \cdot (\theta - \sin(\theta))$. The integration limits $\delta_0$ och $\delta_{ap}$ are calculated by using equitation 5.

Each surface can be calculated by using equation 5 and 6, respectively. The surfaces $A_n$ och $A_t$ are dominant in size, the surface $A_c$ is insignificant. Unfortunately, equation 6 is without any analytical solution, due to the fact that the derivate of the chip area as a function of the angular coordinate lacks a primitive function.

For depths-of-cut $a_p > r$ the true equivalent chip thickness $h_e$ can be calculated by adding the area corresponding to the major cutting edge, as $f(a_p - r) \cdot \sin(\kappa)$, (Ståhl 2007). Furthermore, $h_e$ can be calculated for any arbitrary value of approach angle $\kappa$ by setting the upper integration limit to $\kappa$ in equation 6. Figure 8 illustrates the appearance of the chip area $A$ as a function of $a_p$.

The length of the active cutting edge $l_c$ is built by 3 parts, according to equation 8, for $a_p < r$.

$$l_c = r \cdot (-\delta_0 + \delta_{ap} + \delta_c)$$

(8)

Where $\delta_c$ can be calculated as:

$$\delta_c = 2 \cdot \sin^{-1} \left( \frac{ f \cdot \cos(\delta_{ap}) }{ 2 \cdot r } \right)$$

(9)

The equivalent chip thickness $h_e$ can be calculated by forming the ratio between the chip area $A$ (according to equation 7) and the active cutting length $l_c$ (according to equation 8). By also including the previously presented equations, the equivalent chip thickness $h_e$ can be calculated as:

$$A = A_n + A_t + A_c$$

$$A_n = \int_{\delta_0}^{\delta_{ap}} \int f \cdot \sin(\delta) + r - \sqrt{r^2 - f^2 \cdot \cos^2(\delta_{ap})} \quad d\delta$$

$$A_t = \int \frac{f \cdot \cos(\delta_{ap})}{2}$$

$$A_c = \frac{r^2}{2} \left( \frac{f \cdot \cos^{-1}(\delta_{ap})}{r} - \frac{f \cdot \cos(\delta_{ap})}{r} \right)$$

(7)
\[ h_e = \frac{A}{l_e} \]

\[
= \frac{f \sin(\delta) + r - \sqrt{r^2 - f^2 \cos^2(\delta_{w})}}{2} + \frac{f \cdot \cos(\delta_{w})}{r} + \frac{f^2}{2} \left( \frac{\cos(\delta_{w})}{r} - \frac{\cos(\delta_{w})}{r} \right)
\]

\[ \Delta = \frac{h_e(f, r, a_p) - h_{W}^{e}(f, r, a_p)}{h_e(f, r, a_p)} \cdot 100 \quad (10) \]

In Figure 9, \( h_e \) for \( a_p < r \) and different feeds is presented.

Figure 9. The equivalent chip thickness \( h_e \) as a function of depth-of-cut \( a_p < r \), \( f=0.1 \) mm/rev (lower black curve), \( f=0.2 \) mm/rev (middle blue curve) and \( f=0.4 \) mm/rev (upper red curve) and for nose radius \( r=1.2 \) mm.

### 8. COMPARISON BETWEEN \( h_W^e \) AND \( h_e \)

In Figure 10, a comparison between calculated values of the Woxén chip equivalent (equation 2) and the new solution (equation 9) is illustrated. The deviation between the models varies depending on the range of initial conditions.

Figure 10. Comparison between true chip equivalent \( h_e \) (solid curves) and Woxén chip equivalent \( h_W^e \) (broken curves), as a function of depth-of-cut \( a_p \), \( r=1.2 \) mm, \( f=0.1 \), 0.2 and 0.4 mm/rev.

An error analysis of Woxén's chip equivalent \( h_W^e \) can be performed by formulating a function \( \Delta \) according to equation 10, which describes the relative deviance in % between \( h_W^e \) and \( h_e \).

Without any closer examination it can be deduced that tool life models and systems for choice of cutting data based on Woxén's chip equivalent, provides a very limited precision within the finishing area. The largest error in the calculations of Woxén's chip equivalent is obtained with a depth-of-cut less than \( \frac{1}{4} \) of the nose radius, which in the presented cases corresponds to a depth-of-cut \( a_p \) between 0.3 och 0.4 mm/rev.

A systematic fault in the determination of model constants and the subsequent application of given cutting data recommendations, can to a certain degree limit the effects of the errors in Woxèn's approximation. This is due to the fact that the same cutting data combinations are used both to determine the model constants and in later production and metal cutting.

The basic idea with the chip equivalent is precisely that it is equivalent, meaning all combinations of depth-of-cut and feed that generates the same chip equivalent should also generate the same tool life, under similar conditions. In Figure 12 and Figure 13, contour diagrams illustrate how different combinations of feed and depth-of-cut generate the same chip equivalent. Figure 12 shows the results based on \( h_W^e \), Figure 13 shows the results based on \( h_e \).

### 9. CONCLUSIONS AND DISCUSSION

Without any closer examination it can be deduced that tool life models and systems for choice of cutting data based on Woxén's chip equivalent, provides a very limited precision within the finishing area. The largest error in the calculations of Woxén’s chip equivalent is obtained with a depth-of-cut less than \( \frac{1}{4} \) of the nose radius, which in the presented cases corresponds to a depth-of-cut \( a_p \) between 0.3 och 0.4 mm/rev.

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By using the developed models and equations to determine the chip equivalent, better conditions to predict tool life and tool wear in metal cutting are created. The model can be adapted to other types of inserts, not having the circular bridging between major and minor cutting edge.

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